

PA-IDC

QUERY CONTROL FORM		RTIS USE ONLY	
Application No. <u>10/007,754</u>	Prepared by <u>BSH</u>	Tracking Number <u>05974680</u>	
Examiner-GAU <u>Flynn-2826</u>	Date <u>07-23-04</u>	Week Date <u>6-28-04</u>	
	No. of queries <u>1</u>	<u>IFWE</u>	

JACKET

- | | | | |
|----------------------|------------------------|--------------------|----------------|
| a. Serial No. | f. Foreign Priority | k. Print Claim(s) | p. PTO-1449 |
| b. Applicant(s) | g. Disclaimer | l. Print Fig. | q. PTOL-85b |
| c. Continuing Data | h. Microfiche Appendix | m. Searched Column | r. Abstract |
| d. PCT | i. Title | n. PTO-270/328 | s. Sheets/Figs |
| e. Domestic Priority | j. Claims Allowed | o. PTO-892 | t. Other |

SPECIFICATION

- a. Page Missing
- b. Text Continuity
- c. Holes through Data
- d. Other Missing Text
- e. Illegible Text
- f. Duplicate Text
- g. Brief Description
- h. Sequence Listing
- i. Appendix
- j. Amendments
- k. Other

CLAIMS

- a. Claim(s) Missing
- b. Improper Dependency
- c. Duplicate Numbers
- d. Incorrect Numbering
- e. Index Disagrees
- f. Punctuation
- g. Amendments
- h. Bracketing
- i. Missing Text
- j. Duplicate Text
- k. Other

MESSAGE

PTO 1449 - please either line-through or
initial citations. Copy provided for
reference.

Thank You
initials BSH

RESPONSE

initials

+

Please type a plus sign (+) inside this box. FORM PTO/SB/08

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		<i>Complete if Known</i>			
		Application Number	10/007,754		
		Filing Date			
		First Named Inventor	Darrell G. Hill		
		Group Art Unit			
Examiner Name					
Sheet	2	of	2	Attorney Docket Number	SC10776P C01

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	19	H. Lin et al., "Super-Gain AlGaAs/GaAs Heterojunction Bipolar Transistors Using An Emitter Edge-Thinning Design", American Institute of Physics, appl. Phys. Letter 47(8), 15 October 1985, pages 839-841.	
	20	W. Lee et al., "Effect of Emitter-Base Spacing on the current Gain of AlGaAs/GaAs Heterojunction Bipolar Transistors", IEEE Electronic Device Letters, Vol. 10, No. 5, May 1989, pages 200-202.	
	21	Wolf, S., "Silicon Processing for the VLSI Era, Volume 2", Process Integration, Sunset Beach, CA, 1990, pages 486-488.	

Examiner Signature		Date Considered	
--------------------	--	-----------------	--

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation, if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Unique citation designation number. ² Applicant is to place a check mark here if English Language Translation is attached.